

FIG. 1

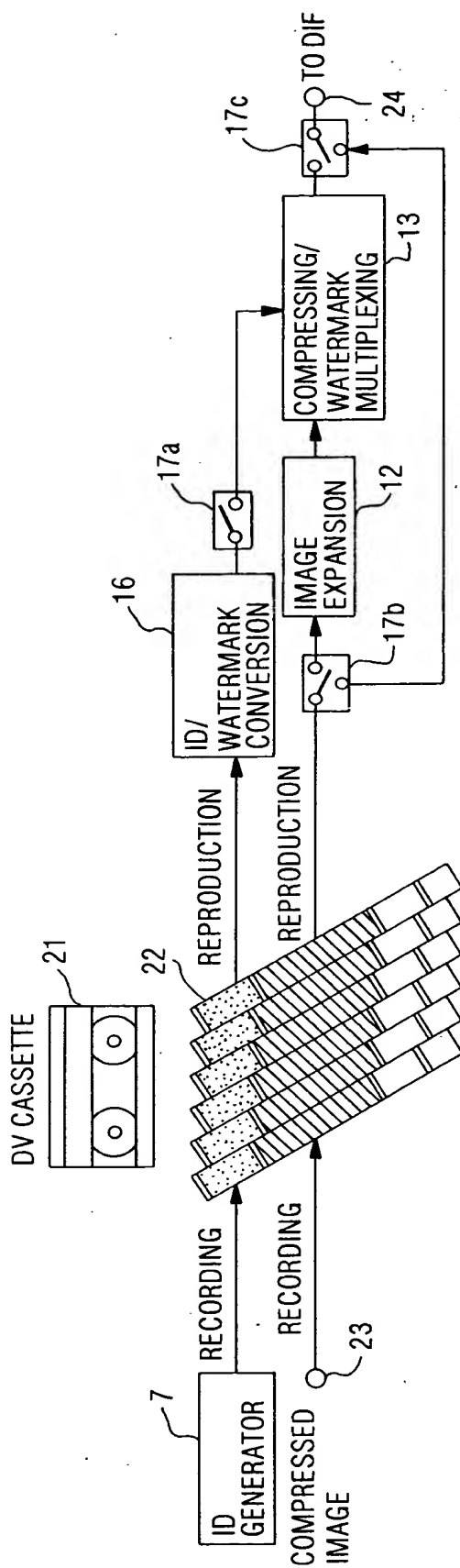


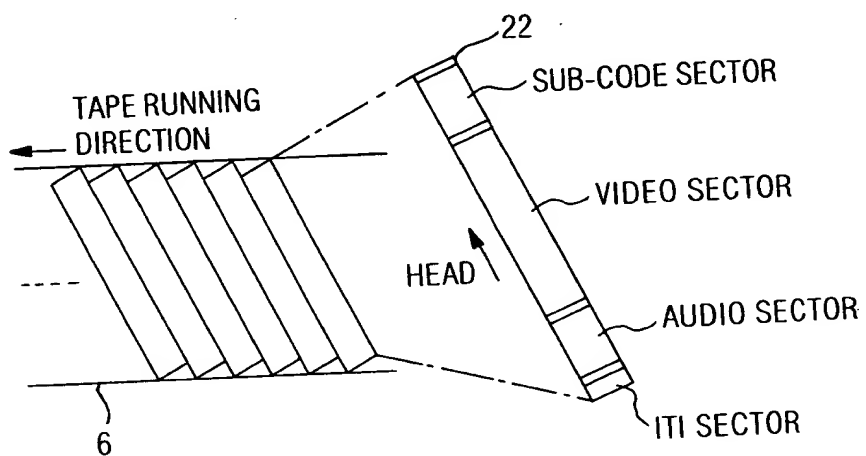
FIG. 2

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FRATISMAN		

**FILE**

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**FIG. 3**

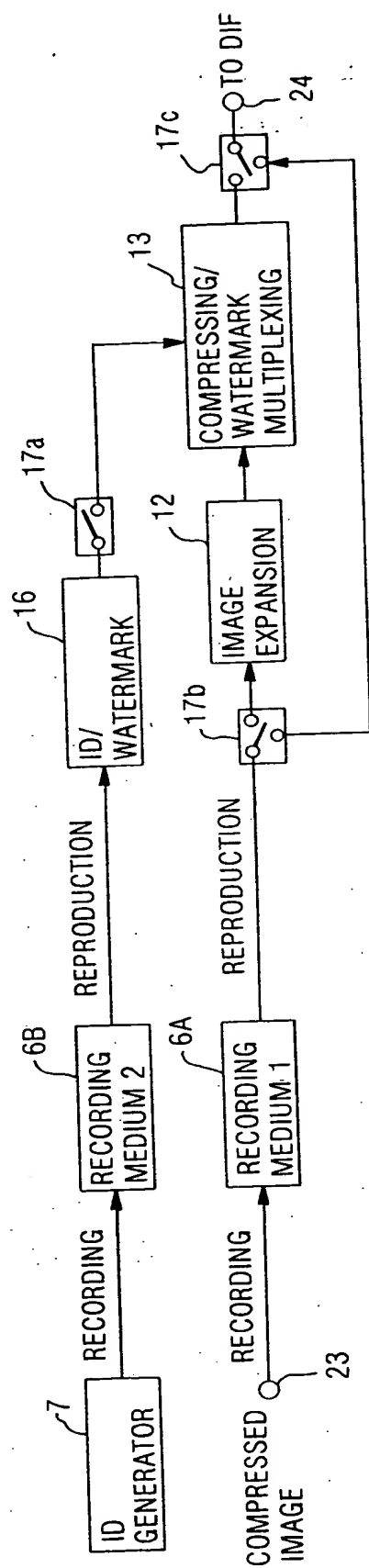


FIG. 4

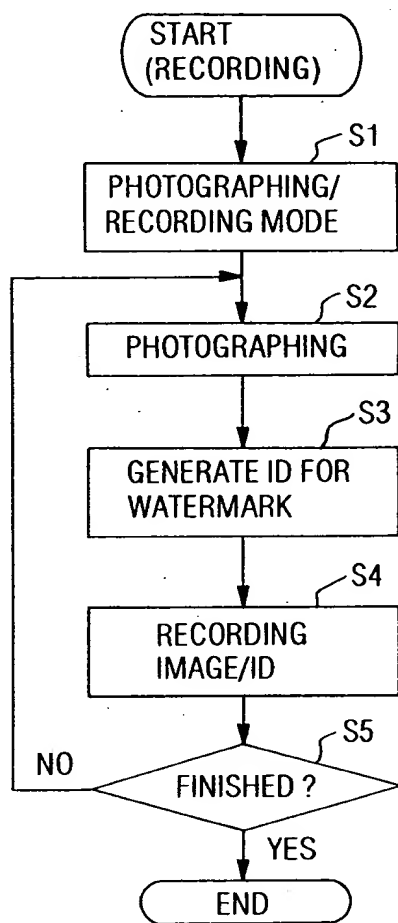


FIG. 5A

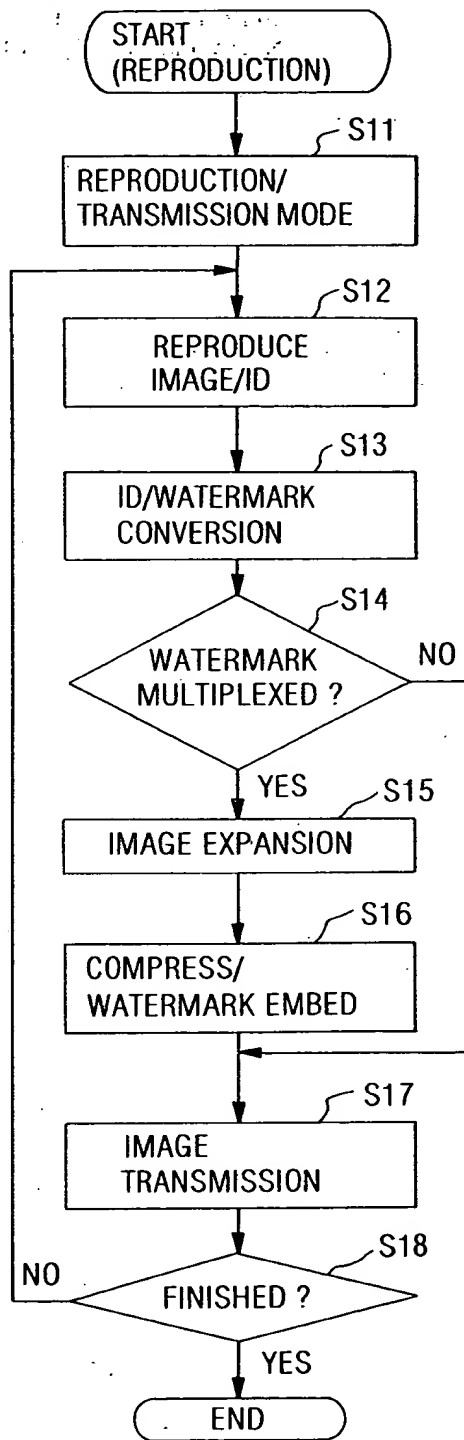


FIG. 5B

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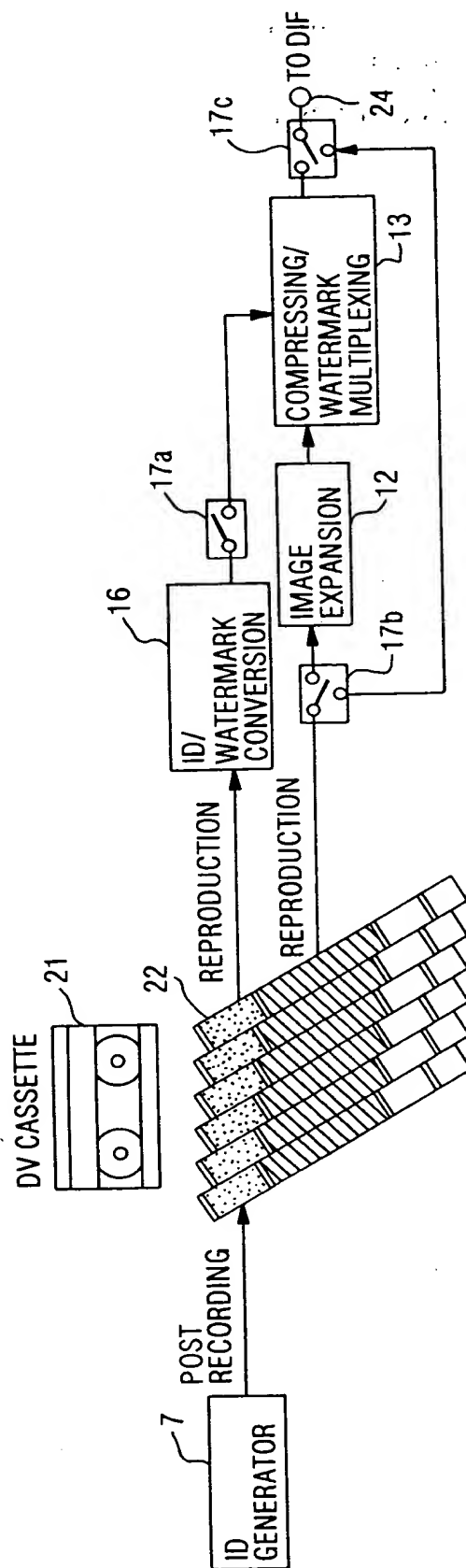
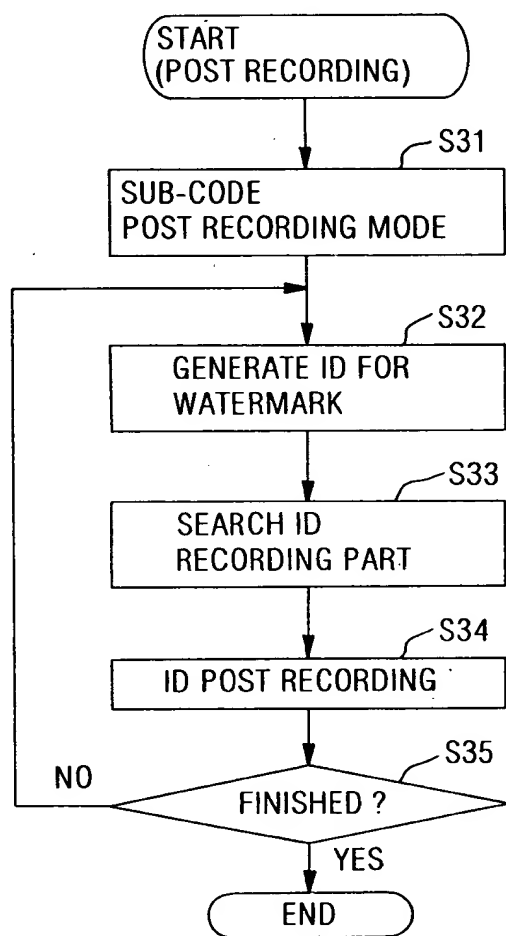


FIG. 6



**FIG. 7**

SOURCE PACKET

0	H0	SC0	SC1	VA0	VA1	VA2	DIF SEQUENCE 0
1	A0	V0	V1	V2	V3	V4	
2	V5	V6	V7	V8	V9	V10	
23	V123	V124	V125	V126	V127	V128	DIF SEQUENCE 0
24	V129	V130	V131	V132	V133	V134	
25	H0	SC0	SC1	VA0	VA1	VA2	DIF SEQUENCE 1
49	V129	V130	V131	V132	V133	V134	
225	H0	SC0	SC1	VA0	VA1	VA2	DIF SEQUENCE 9
249	V129	V130	V131	V132	V133	V134	

H0 : HEADER DIF BLOCK

SCi : SUB-CODE DIF BLOCK (i=0, 1)

VAi : VAUX DIF BLOCK (i=0, 1, 2)

Ai : AUDIO DIF BLOCK (i=0, ...8)

Vi : VIDEO DIF BLOCK (i=0, ...134)

FIG. 8



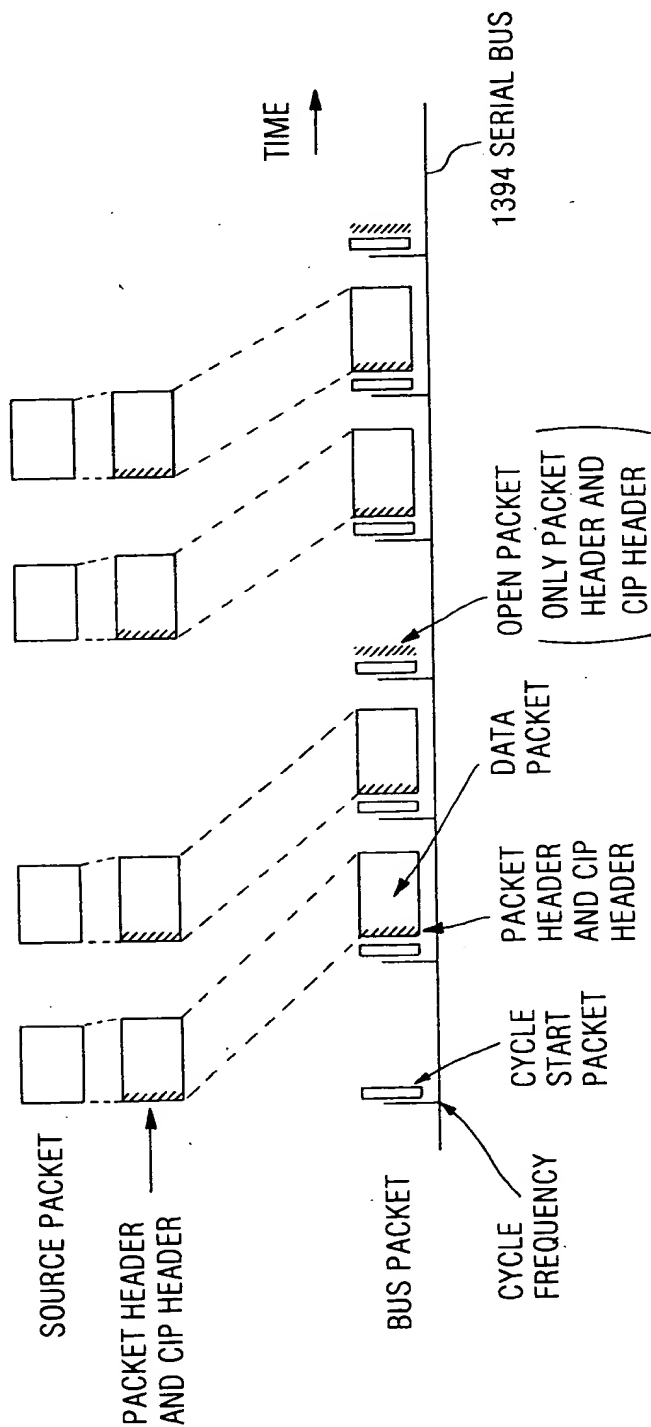


FIG. 9

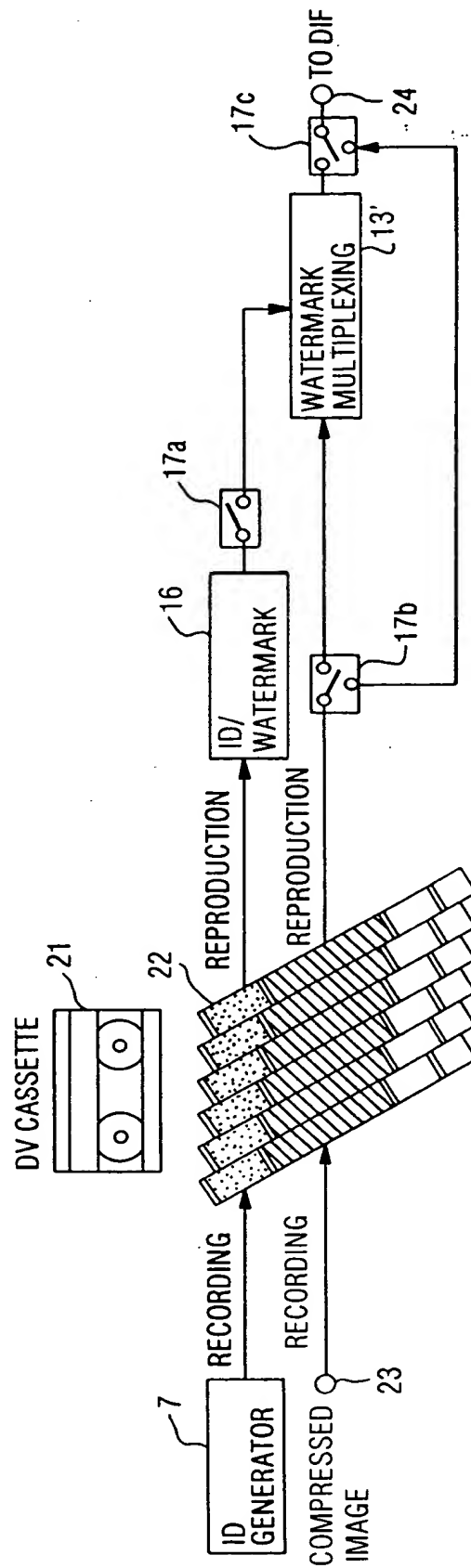


FIG. 10

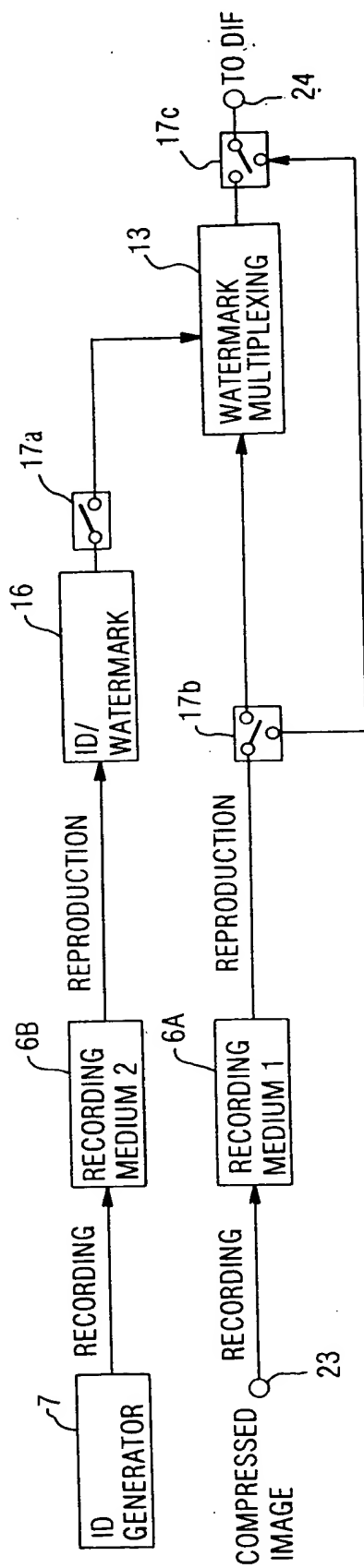


FIG. 11

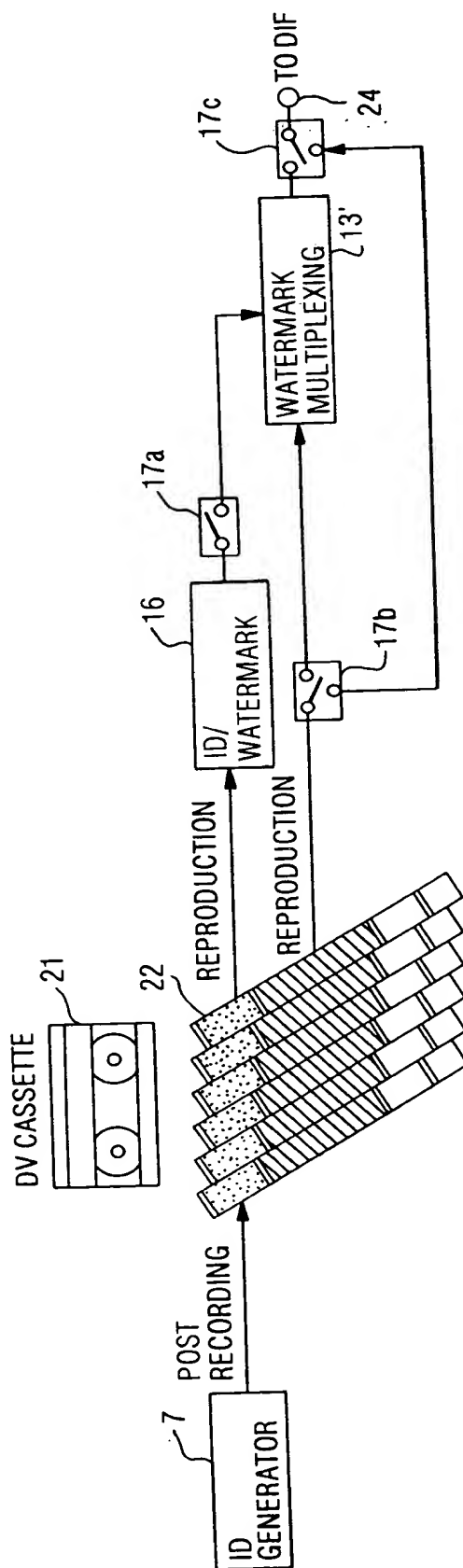


FIG. 12

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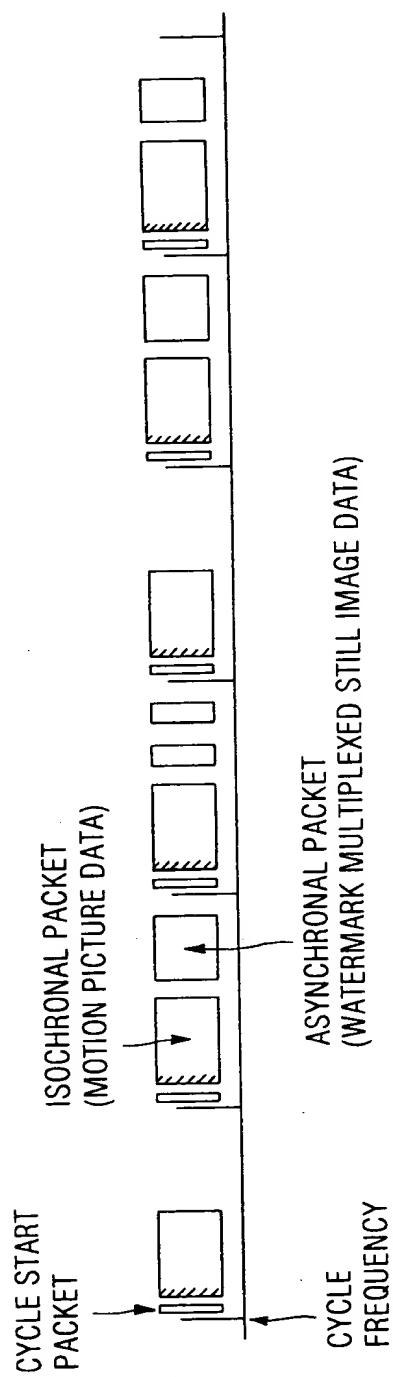


FIG. 13

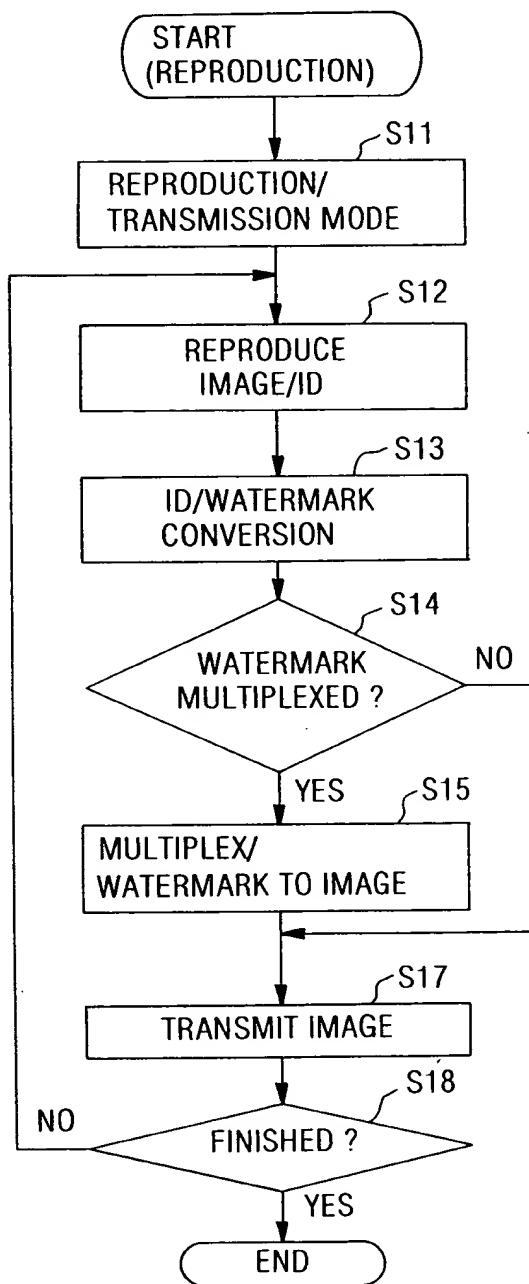


FIG. 14

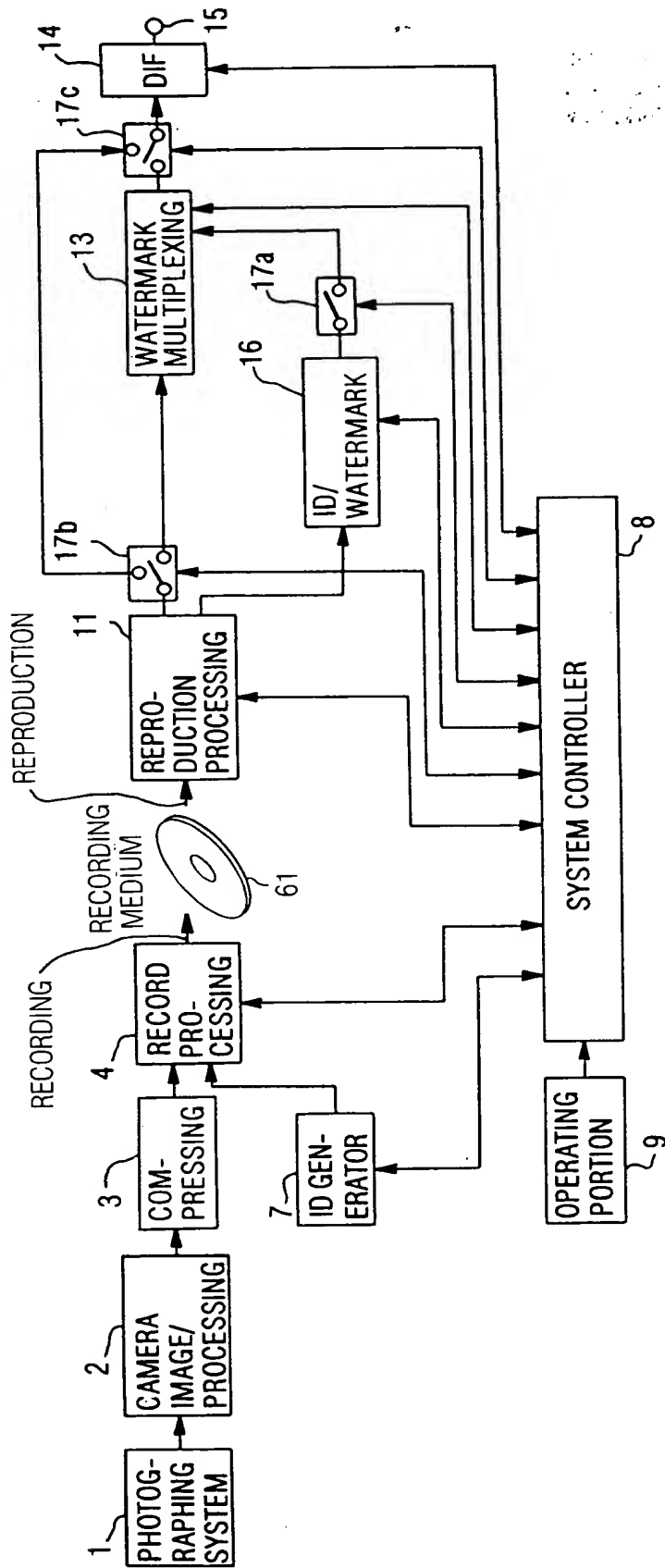


FIG. 15

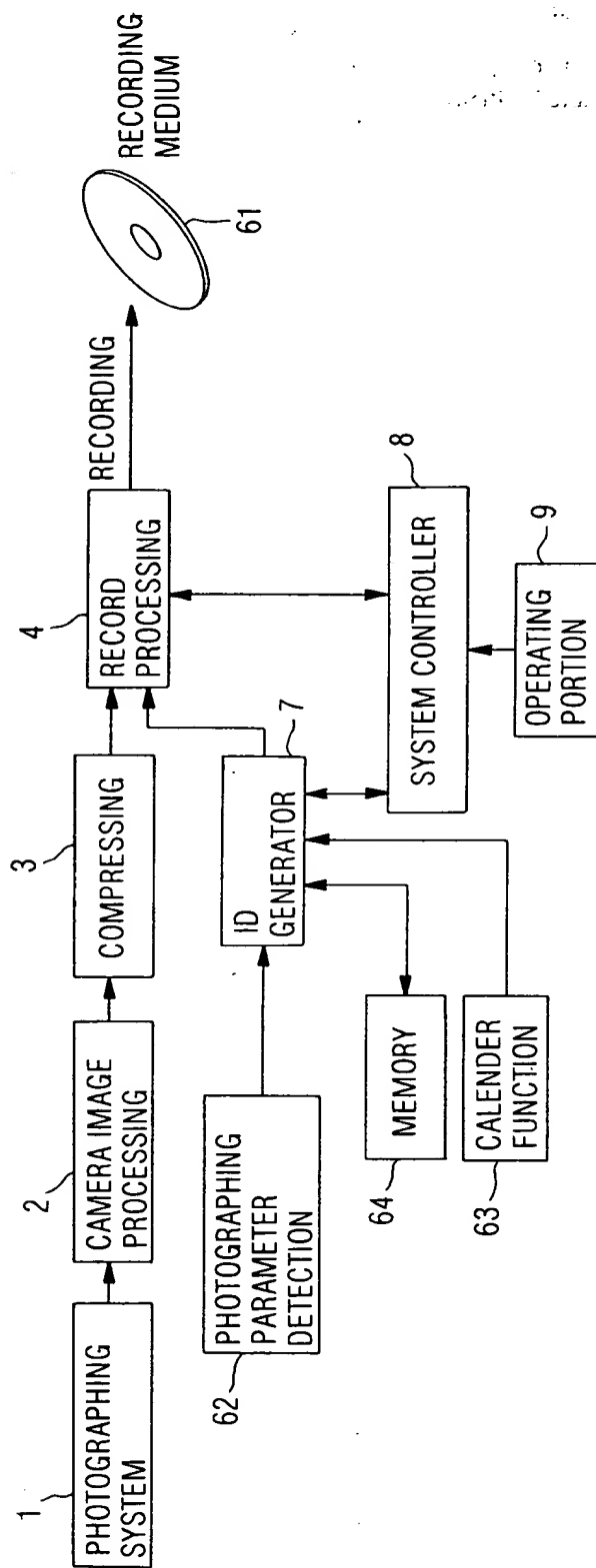
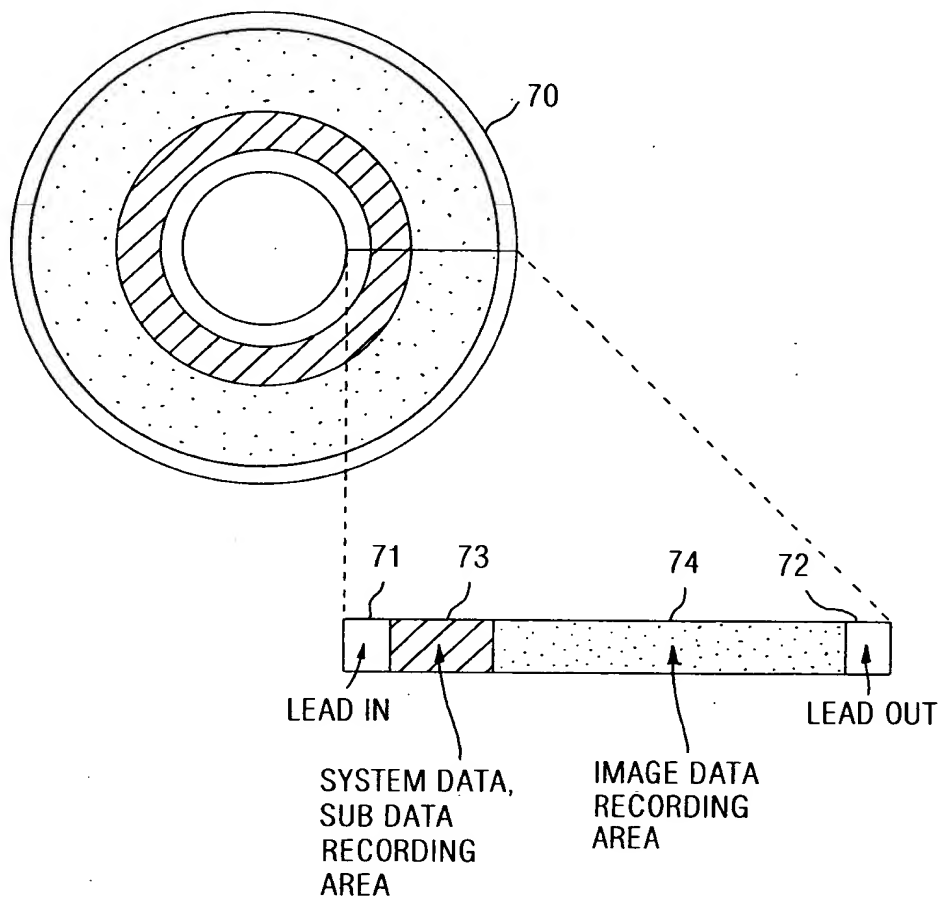


FIG. 16





**FIG. 17**

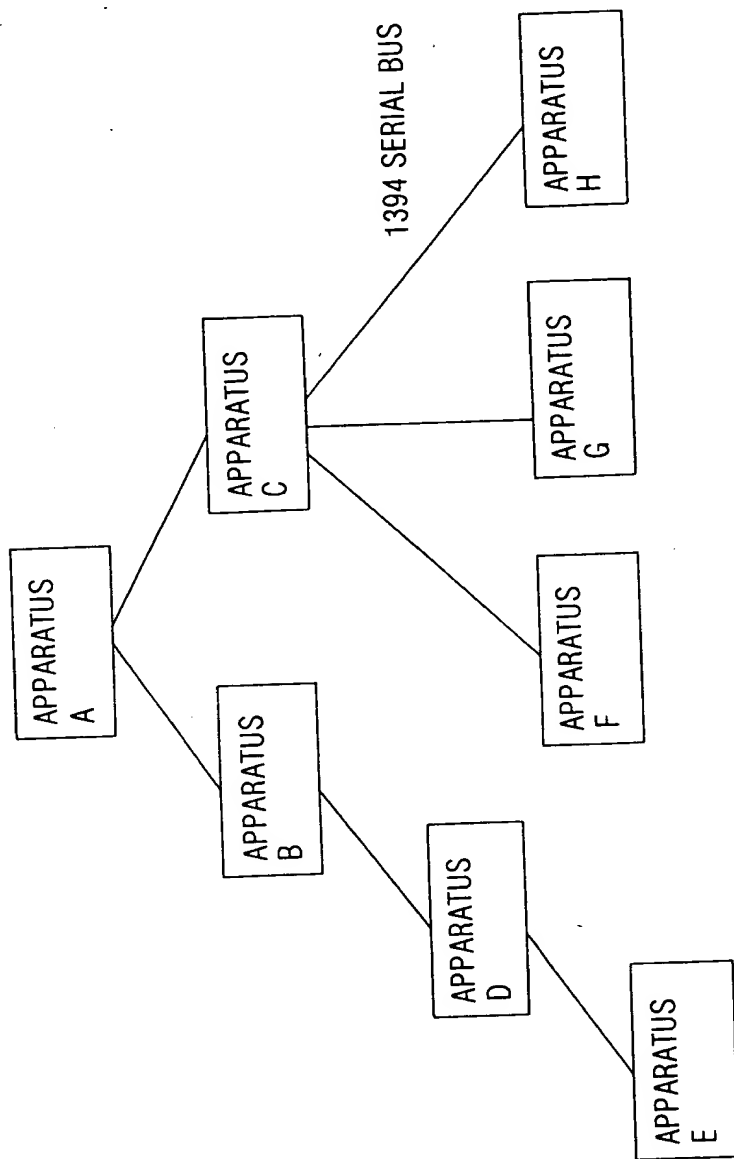


FIG. 18

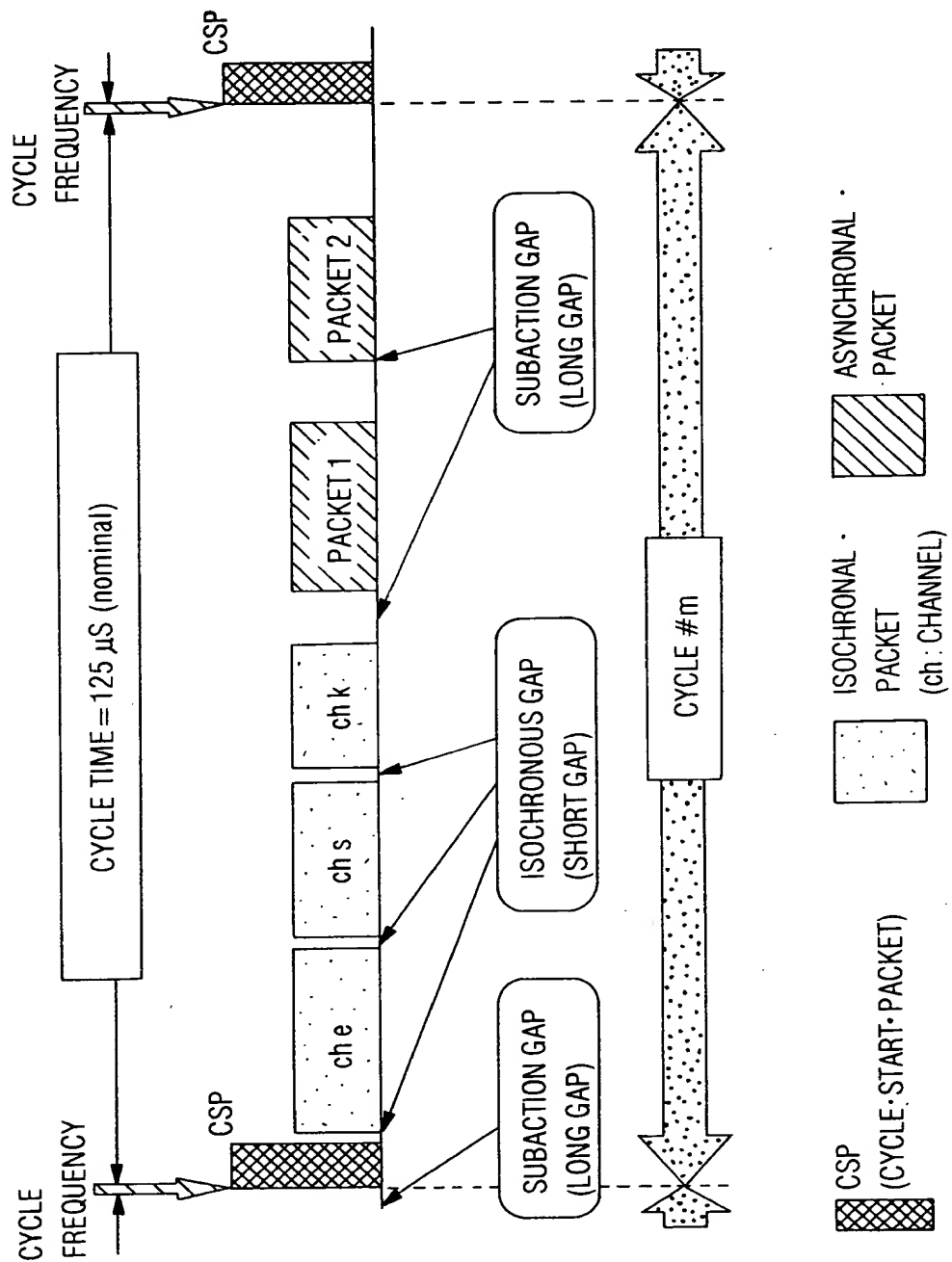


FIG. 19

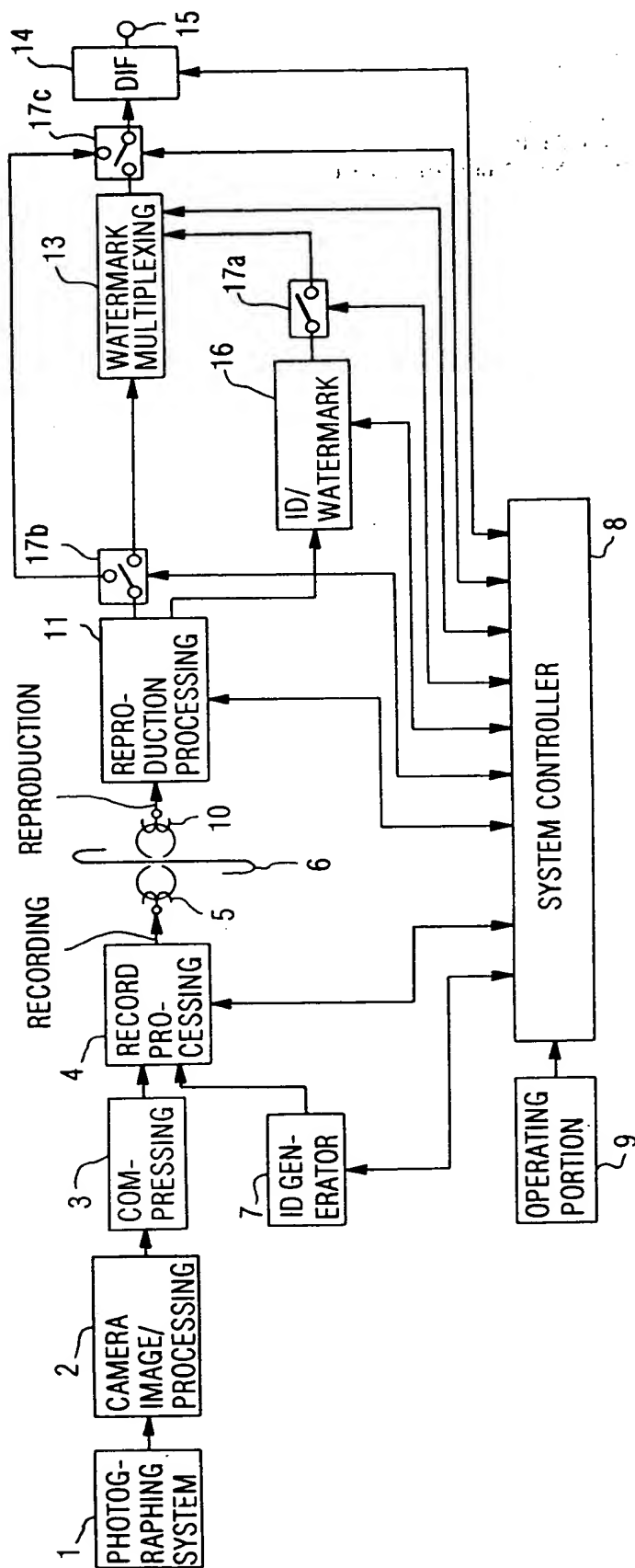


FIG. 20